CLAIMS

What is claimed is:

1. A processor comprising:

    a voltage regulator to be powered by a first voltage and to provide a second voltage; and

    a circuit powered by the second voltage.

2. The processor of claim 1, wherein the second voltage is adjustable by the processor.

3. The processor of claim 2, wherein the voltage regulator includes a digitized resistor to be set by the processor.

4. The processor of claim 1, wherein the second voltage is to be set to allow the circuit to meet a timing requirement.

5. The processor of claim 1, further comprising a port to receive the first voltage from an external voltage regulator.

6. The processor of claim 1, wherein the voltage regulator includes an op amp.

7. The processor of claim 6, wherein the circuit is a digital circuit.

8. The processor of claim 1, wherein the circuit includes at least a portion of a core of the processor.

9. The processor of claim 1, wherein the circuit includes a memory region.

10. The processor of claim 9, wherein the memory region is a cache.

11. A computer system comprising:

a discrete voltage regulator to provide a global Vcc; and

a processor including a local voltage regulator to be powered by the global Vcc and to provide a local Vcc for the processor.

12. The computer system of claim 11, wherein the local Vcc is adjustable by the processor.

13. The computer system of claim 12, wherein the local voltage regulator includes a digitized resistor to be set by the processor.

14. The computer system of claim 11, wherein the processor includes a cache to be powered by the local Vcc.

15. The computer system of claim 11, wherein the processor is a graphics controller.

16. A method comprising:

providing a first voltage to a processor comprising an integrated voltage regulator;

powering the voltage regulator with the first voltage, the voltage regulator to provide a second voltage; and

powering at least a portion of the processor with the second voltage.

17. The method of claim 16, further comprising adjusting the second voltage by the processor.

18. The method of claim 16, wherein powering at least a portion of the processor includes powering a floating point unit of the processor.